

This listing of claims will replace all prior versions and listings of claims in the application:

**Listing of Claims:**

1.-71. (Canceled).

72. (Currently Amended) A method for operating a memory card that includes 1) a host controller configured to communicate with a host device, 2) at least an initial volume and 3) a switch ~~and that wherein the memory card~~ provides non-volatile data storage having an address space defined by a contiguous range of addresses, the method in the host controller comprising:

(a) retrieving volume information from ~~an~~ the initial volume stored in a range of addresses that is a part of the contiguous range of addresses that defines the address space;

(b) determining, based on the volume information, whether the initial volume uses a 16-bit addressing or uses less than the 16-bit addressing;

(c) when said determining (b) determines the initial volume uses greater than the 16-bit addressing, by-passing a switch position of the switch, determining the memory card uses 32-bit addressing and communicating to the host via the host controller to use the memory card as a single volume using 32-bit addressing and; and

(d) when said determining (b) determines that the initial volume uses the 16-bit addressing or uses less than the 16-bit addressing,

(1) reading a the switch position of a the switch on the memory card wherein the switch position of the switch is only used when the host controller determines 16-bit addressing or less is used;

(2) determining an address offset for the address space based on upon the switch position wherein the address offset corresponds to one of a plurality of volumes in which the memory card is partitioned;

(3) communicating to the host device via the host controller to use the volume of the memory card indicated by the switch position ~~as one of a plurality of volumes~~ and the address offset.

73. (Previously Presented) A method as recited in claim 72,  
wherein the switch has at least a first position and a second position,  
wherein, when the switch position is in the first position and the memory card is operated by dividing the address space of the non-volatile data storage into the plurality of volumes, the first volume of the non-volatile data storage is accessed, and  
wherein, when the switch position is in the second position and the memory card is operated by dividing the address space of the non-volatile data storage into the plurality of volumes, a second volume of the non-volatile data storage is accessed.

74. (Previously Presented) A method as recited in claim 73, wherein the memory card is formatted into either one of a single volume or a pair of volumes, the pair of volumes being the first volume and the second volume.

75. (Previously Presented) A method as recited in claim 74, wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the first and second volumes of X/2 gigabytes each as the pair of volumes.

76. (Previously Presented) A method as recited in claim 72, wherein said method further comprises:

detecting activation of the memory card, and  
wherein said retrieving (a) and said determining (b) are performed after said detecting detects the activation of the memory card.

77. (Previously Presented) A method as recited in claim 76, wherein the activation of the memory card occurs upon power-on of the memory card or upon insertion of the memory card into a host device.

78. (Previously Presented) A method as recited in claim 72,  
wherein the memory card is formatted into a single volume or a plurality of volumes, and  
wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the N volumes of X/N gigabytes each as the plurality of volumes.

79. (Currently Amended) A method as recited in claim 72, wherein when said determining (b) determines the initial volume uses greater than the 16-bit addressing,[[,]] the initial volume has a FAT-32 file format.

80. (Previously Presented) A method as recited in claim 72, when said determining (b) determines that the initial volume uses the 16-bit addressing or uses less than the 16-bit addressing, each of the multiple volumes has a FAT-16 file format.